

**DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN
APPLICATION DATA SHEET (37 CFR 1.76)**

Electronic Version v11

Stylesheet Version v10

Title of Invention	NONVOLATILE SEMICONDUCTOR MEMORY DEVICE HAVING DIVIDED BIT LINES
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As the below named inventors, we declare that:

This declaration is directed to the invention titled: " NONVOLATILE SEMICONDUCTOR MEMORY DEVICE HAVING DIVIDED BIT LINES"

We believe that we are the original and first inventors of the subject matter which is claimed and for which a patent is sought;

We have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to above;

We acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to us to be material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT International filing date of the continuation-in-part application.

All statements made herein of own knowledge are true, all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001, and may jeopardize the validity of the application or any patent issuing thereon.

FULL NAME OF INVENTORS:

Inventor 1: Ching-Hsiang Hsu	Inventor
Signature :	Citizen of : TW
Inventor 2: Ching-Sung Yang	Inventor
Signature :	Citizen of : TW

COMBINED DECLARATION AND POWER OF ATTORNEY

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name and that I believe I am an original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

NONVOLATILE SEMICONDUCTOR MEMORY DEVICE HAVING DIVIDED BIT LINES

the specification of which

is attached hereto.

was filed on _____

as Application Serial No. _____ and was amended on _____.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s):

Number	Country	Date Filed(yyyy/mm/dd)	Yes	No
90107844	Taiwan, R.O.C.	2001/4/2		X

I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

Jiawei Huang	(Reg. No. 43,330)	Charles C.H. Wu	(Reg. No. 39,081)
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COMBINED DECLARATION AND POWER OF ATTORNEY CONTINUED

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patents issued thereon.

Signature: Ching-Hsiang Hsu Date: Aug. 27, 2001

Sole or First Joint Inventor: Ching-Hsiang Hsu

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Signature: Ching-Song Yang Date: Aug. 27, 2001

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